

Микросхема интегральная 1892ВМ14Я. Расположение выводов на корпусе



	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36
A	GPIOA0/TRACE_D0	GPIOA2/TRACE_D2	GPIOA4/TRACE_D4	GPIOA6/TRACE_D6	GPIOA18/UART0_SIN	GPIOA21/UART0_TSN	GPIOA23/SPI1_TXD	UART3_SIN	GPIOD09/TS_SDO0	GPIOD17/SPI0_RXD	GPIOD25/DC2_SCL	GPIOD30/VPIN_VDI0	GPIOD32/VPIN_VDI2	GPIOD34/VPIN_VDI4	GPIOD36/VPIN_VDI6	GPIOD38/VPIN_VDI8	GPIOD40/VPIN_VDI10	GPIOD42/VPIN_VDI12	GPIOD44/VPIN_VDI14	VPIN_FSY_NC9	GPIOD39/VPIN_RST00	GND	VPIN_PX_CLK00	SW1_DIN P	SW1_SNP	SW1_SOUT P	SW1_DOUT P	VPOUT_VCLK	VPOUT_VD07	VPOUT_VD011	VPOUT_VD015	VPOUT_VD019	VPOUT_VD023	VPOUT_VD027		
B	GPIOA1/TRACE_D1	GPIOA3/TRACE_D3	GPIOA5/TRACE_D5	GPIOA7/TRACE_D7	GPIOA19/UART0_SOUT	GPIOA20/UART0_CTSN	GPIOA24/SPI1_RXD	UART3_SOUT	GPIOD08/TS_SDI	GPIOD16/SPI0_TXD	GPIOD24/DC2_SDA	GPIOD31/VPIN_VDI1	GPIOD33/VPIN_VDI3	GPIOD35/VPIN_VDI5	GPIOD37/VPIN_VDI7	GPIOD39/VPIN_VDI9	GPIOD41/VPIN_VDI11	GPIOD43/VPIN_VDI13	GPIOD45/VPIN_VDI15	VPIN_FSYNC1	GPIOD31/VPIN_RST01	GND	VPIN_PX_CLK01	SW1_DIN N	SW1_SIN N	SW1_SOUT N	SW1_DOUT N	VPOUT_VSYNC	VPOUT_VD08	VPOUT_VD010	VPOUT_VD014	VPOUT_VD018	VPOUT_VD022	VPOUT_VD026		
C	GPIOA8/TRACE_D8	GPIOA10/TRACE_D10	GPIOA12/TRACE_D12	GPIOA14/TRACE_D14	GPIOA16/TRACE_C_TL	GPIOA22/SPI1_SCL_K	GPIOA25/SPI1_SSN0	UART1_SIN	GPIOD07/TS_SCL_K O	GPIOD15/SPI0_SCL_K	GPIOD23/DC1_SCL	GPIOD30/PWM_TU0	GPIOD37/SPI1_S0	GPIOD39/SPI1_Q0	GPIOD41/SPI1_B0	GPIOD43/SPI1_Q0	GPIOD45/VPIN_VDI16	GPIOD47/VPIN_VDI18	GPIOD49/VPIN_VDI20	GPIOD51/VPIN_VDI22	GPIOD53/VPIN_VDI24	GPIOD55/VPIN_VDI26	GPIOD57/VPIN_VDI28	SW0_DOUT P	SW0_SOUT P	SW0_SNP	SW0_DIN P	VPOUT_HSYNC	VPOUT_VD04	VPOUT_VD09	VPOUT_VD013	DSI_DATAN0/CSI1_DATAN0	DSI_DATA0/CSI1_DATA0	CSB0_DATAN0	CSB0_DATA0	
D	GPIOA9/TRACE_D9	GPIOA11/TRACE_D11	GPIOA13/TRACE_D13	GPIOA15/TRACE_D15	GPIOA17/TRACE_C_CLK	GND	GPIOA26/SPI1_SSN1	GPIOD1/UART1_SOUT	GPIOD06/TIM1_TGL	GPIOD14/TS_SCL_K	GPIOD22/DC1_SDA	GPIOD31/PWM_TU1	MCC_PPS	GPI1_I1	GPI1_Q1	GPI1_B1	GPIOD17/VPIN_VDI17	GPIOD19/VPIN_VDI19	GPIOD21/VPIN_VDI21	GPIOD23/VPIN_VDI23	GPIOD25/VPIN_VDI25	GPIOD27/VPIN_VDI27	GPIOD29/VPIN_VDI29	SW0_DOUT N	SW0_SOUT N	SW0_SIN N	SW0_DIN N	VPOUT_VDEN	VPOUT_VD05	VPOUT_VD08	VPOUT_VD012	DSI_DATAN1/CSI1_DATAN1	DSI_DATA1/CSI1_DATA1	CSB0_DATAN1	CSB0_DATA1	
E	SMC_DATA1/6/GPIOB22	SMC_DATA7/GPIOB23	SMC_DATA14/GPIOB24	SMC_DATA15/GPIOB25	GND	GND	GPIOA27/SPI1_SSN2	GPIOD4/UART2_SIN	GPIOD13/TS_W5	GPIOD21/SPI0_SSN3	GPIOD28/PWM_OUTB0	GPIOD19/SPI0_SSN1	GPIOD26/PWM_OUTA0	VDDPST	VDDPST	GLN2_B0	GLN2_Q0	MCC_CLK IN	SW0_VDD25	SW0_GND25	SW0_VDD11	SW0_GND11	SW1_VDD25	SW1_GND25	SW1_VDD11	SW1_GND11	VDDPST	DSI_GND AC	DSI_GND AC	DSI_VDD AC	DSI_DATAN2/CSI1_DATAN2	DSI_DATA2/CSI1_DATA2	CSB0_DATAN2	CSB0_DATA2		
F	SMC_DATA4/GPIOB20	SMC_DATA5/GPIOB21	SMC_DATA12/GPIOB28	SMC_DATA13/GPIOB29	GND	GND	GPIOA28/SPI1_SSN3	GPIOD5/UART2_SOUT	GPIOD10/TS_SDO1	GPIOD20/SPI0_SSN2	GPIOD29/PWM_OUTB1	GPIOD18/SPI0_SSN0	GPIOD27/PWM_OUTA1	VDDPST	VDDPST	GLN1_Q1	GLN2_I1	GLN2_Q1	SW0_VDD25	SW0_GND25	SW0_VDD11	SW0_GND11	SW1_VDD25	SW1_GND25	SW1_VDD11	SW1_GND11	VDDPST	DSI_GND AC	DSI_GND AC	DSI_VDD AC	DSI_DATAN3/CSI1_DATAN3	DSI_DATA3/CSI1_DATA3	CSB0_DATAN3	CSB0_DATA3		
G	SMC_DATA2/GPIOB18	SMC_DATA3/GPIOB19	SMC_DATA10/GPIOB26	SMC_DATA11/GPIOB27	GND	GND	GND	GND	GPIOA29/DC0_SDA	GPIOD2/UART1_C_TSN	GND	GND	VDDPST	VDDPST	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDPST	DSI_VDD AC	DSI_GND AC	DSI_VDD AC	DSI_CLKP/CSI1_CLK P	DSI_CLKN/CSI1_CLK N	CSB0_CLK P	CSB0_CLKN		
H	SMC_DATA6/GPIOB16	SMC_DATA7/GPIOB17	SMC_DATA8/GPIOB24	SMC_DATA9/GPIOB25	GND	GND	GND	GND	GPIOA30/DC0_SCL	GPIOD3/UART1_RTSN	GPIOD12/TS_SDO3	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDPST	VDDPST	VDDPST	GND	GND	GND	GND	GND		
J	SMC_ADD14/GPIOB6	SMC_ADD15/GPIOB7	SMC_ADD22/GPIOB14	SMC_ADD23/GPIOB15	VDDPST	VDDPST	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND		
K	SMC_ADD12/GPIOB4	SMC_ADD13/GPIOB5	SMC_ADD20/GPIOB12	SMC_ADD21/GPIOB13	VDDPST	VDDPST	GND	GND	GND	GND	GND	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND		
L	SMC_ADD10/GPIOB2	SMC_ADD11/GPIOB3	SMC_ADD18/GPIOB10	SMC_ADD19/GPIOB11	VDDPST	VDDPST	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND		
M	SMC_ADD8/GPIOB8	SMC_ADD9/GPIOB9	SMC_ADD16/GPIOB8	SMC_ADD17/GPIOB9	VDDPST	VDDPST	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND		
N	SMC_ADD6	SMC_ADD7	SMC_BLS_N0	SMC_BLS_N1	SMC_ADV_N	VDDPST	GND	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND		
P	SMC_ADD4	SMC_ADD5	SMC_BAA	SMC_CRE	SMC_WEN	VDDPST	GND	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND		
R	SMC_ADD2	SMC_ADD3	SMC_CSN0	SMC_CSN1	SMC_OEN	VDDPST	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND		
T	SMC_ADD0	SMC_ADD1	SMC_CLK00	SMC_CLK01	SMC_FBC_LK	GND	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND		
U	GMI_TX_D6	GMI_TX_D7	GMI_RX_D6	GMI_RX_D7	GND	GND	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND		
V	GMI_TX_D4	GMI_TX_D5	GMI_RX_D4	GMI_RX_D5	GND	GND	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND		
W	GMI_TX_D2	GMI_TX_D3	GMI_RX_D2	GMI_RX_D3	GND	GND	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND		
Y	GMI_TX_D0	GMI_TX_D1	GMI_RX_D0	GMI_RX_D1	GND	GND	GND	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND		
AA	GMI_TX_S	GMI_C0K	GMI_S0C	GMI_TX_S	GMI_CKS	GND	GND	APLL_VDDAC	APLL_GNDAC	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND		
AB	GMI_RXE_R	GMI_TXC_LK	GMI_M0	GMI_RX_DV	GMI_RX_CLK	GND	GND	SPLL_VDDAC	SPLL_GNDAC	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND		
AC	SDMMC1_DATA3	SDMMC1_DATA2	SDMMC1_DATA1	SDMMC1_DATA0	SDMMC1_VDD	ALIVE_VDD	ALIVE_VDD	SWPLL_VDDAC	SWPLL_GNDAC	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND	GND	
AD	SDMMC1_DATA7	SDMMC1_DATA6	SDMMC1_DATA5	SDMMC1_DATA4	SDMMC1_GND	ALIVE_VDD	ALIVE_VDD	SWPLL_VDDAC	SWPLL_GNDAC	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	GND	GND	
AE	SDMMC1_IEN	SDMMC1_CMD	SDMMC1_CLK	SDMMC1_DIETN	SDMMC1_GND	ALIVE_VDD	ALIVE_VDD	SWPLL_VDDAC	SWPLL_GNDAC	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	
AF	SDMMC0_DATA3	SDMMC0_DATA2	SDMMC0_DATA1	SDMMC0_DATA0	SDMMC0_VDD	SIGRES10	SIGRES8	WPLL_VDDAC	WPLL_GNDAC	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	GND	
AG	SDMMC0_DATA7	SDMMC0_DATA6	SDMMC0_DATA5	SDMMC0_DATA4	SDMMC0_GND	SIGRES15	SIGRES14	WPLL_VDDAC	WPLL_GNDAC	SIGRES0	VDDPST	VDDPST	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	
AH	SDMMC0_IEN	SDMMC0_CMD	SDMMC0_CLK	SDMMC0_DIETN	SDMMC0_GND	SIGRES17	SIGRES16	SIGRES7	SIGRES4	SIGRES3	VDDPST	VDDPST	VDDPST	VDDPST	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	VDD	VDD	GND	GND	GND	GND	GND	
AJ	NAND_CS_N0	NAND_CS_N1	NAND_D_Q5	NAND_W_RN	NAND_RB_N0	SIGRES11	SIGRES12	SIGRES6	SIGRES2	TESTEN	OTC_VDD13	OTC_VDD3	OTC_VDD3	OTC_VDD_PST	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	
AK	NAND_CS_E	NAND_AL_E	NAND_D_ATA8	NAND_D_ATA9	NAND_RB_N1	SIGRES13	SIGRES9	SIGRES5	SIGRES1	TESTEN	OTC_VDD13	OTC_VDD3	OTC_VDD3	OTC_VDD_PST	RTC_VDD	RTC_WA_KEEP	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	
AL	NAND_DATA0	NAND_D_ATA3	NAND_D_ATA10	NAND_D_ATA11	XTI_24M	OTFRAM_BYP	OTFRAM_EST	OTFRAM_EST	TESTMOD_P_LL	TESTCLK	OTC_GND AC	OTC_GND AC	OTC_GND AC	OTC_VDD AC	OTC_VDD	OTC_ISO	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	
AM	NAND_DATA2	NAND_D_ATA3	NAND_D_ATA12	NAND_D_ATA13	NAND_RD_N	OTFCUR_STDISABLE	OTFCUR_BYPASS	OTFCUR_BYPASS	TESTI2C	OTC_VDD25	OTC_GND A	DDRI_GND DQ	DDRI_GND DQ	DDRI_GND DQ	DDRI_GND DQ	DDRI_DM3	DDRI_DM2	DDRI_VR_EF	DDRI_CK_E0	DDRI_CA_SN	DDRI_AD_D5	DDRI_AD_D14	DDRI_AD_D11	DDRI_AD_D2	DDRI_AT_0	DDRI_PZ_Q	DDRI_DM1	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	
AN	NAND_DATA4	NAND_D_ATA5	NAND_D_ATA14	NAND_D_ATA15	NAND_RD_N	OTFCUR_STDISABLE	OTFCUR_BYPASS	OTFCUR_BYPASS	TESTI2C	OTC_VDD25	OTC_GND A	DDRI_GND DQ	DDRI_GND DQ	DDRI_GND DQ	DDRI_DM3	DDRI_DM2	DDRI_VR_EF	DDRI_CK_E1	DDRI_CA_SN	DDRI_AD_D6	DDRI_AD_D15	DDRI_AD_D13	DDRI_RA_SN	DDRI_OD_T1	DDRI_DQ12	DDRI_DQ13	DDRI_DQ3	DDRI_DQ51	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	
AP	NAND_DATA6	NAND_D_ATA7	base_VDD A	GND	SYSTEM_OFF	OTFCUR_STDISABLE	OTFCUR_BYPASS	OTFCUR_BYPASS	TESTI2C	OTC_VDD25	OTC_GND A	DDRI_GND DQ	DDRI_GND DQ	DDRI_GN DQ	DDRI_DM3	DDRI_DM2	DDRI_VR_EF	DDRI_CK_E1	DDRI_CA_SN	DDRI_AD_D6	DDRI_AD_D15	DDRI_AD_D13	DDRI_RA_SN	DDRI_OD_T1	DDRI_DQ14	DDRI_DQ15	DDRI_DQ1	DDRI_DQ51	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	
AR	WIRQ1	BOOT2	BOOT0	NRST_PO_N	NV_MODE_0	OTFCUR_STDISABLE	OTFCUR_BYPASS	OTFCUR_BYPASS	TESTI2C	OTC_VDD25	OTC_GND A	DDRI_GND DQ	DDRI_GN DQ	DDRI_DQ5_B3	DDRI_DQ30	DDRI_DQ31	DDRI_DQ18	DDRI_DQ17	DDRI_AD_D0	DDRI_AD_D7	DDRI_CK_N	DDRI_AD_D3	DDRI_WE_N	DDRI_DT_01	DDRI_DQ10	DDRI_DQ11	DDRI_DQ5	DDRI_DQ50	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	DDRI_GN DQ	
AT	WIRQ0	BOOT1	NRST_WA_RM	NV_MODE_1	IMODE0	OTFCUR_STDISABLE	OTFCUR_BYPASS	OTFCUR_BYPASS	TESTI2C	OTC_VDD25	OTC_GND A	DDRI_GN DQ	DDRI_DQ5_B2	DDRI_DQ24	DDRI_DQ25	DDRI_DQ20	DDRI_DQ23	DDRI_CS_N1	DDRI_AD_D8	DDRI_AD_D1	DDRI_CK	DDRI_AD_D1	DDRI_BA_2	DDRI_DT_00	DDRI_DQ8	DDRI_DQ9	DDRI_DQ									